PN   -   KR9615603 B 19961118
TI   -   AAL5 COMMON PART CONVERGENCE SUBLAYER AND RECONSTRUCTION METHOD BY RESPECTIVE BUFFER METHOD

AB   -   The receiving part and reassembly circuit comprises: an ATM interface means(4) performing interface with ATM layers; a reassembly buffer(7) storing the reassembled cell; a control buffer controlling means(6) judging the reassembly state; a control buffer(5) storing the information of the reassembly buffer(7); a message buffer controlling part(8) performing CRC and removing padding; a message buffer(9) storing the message transmitted from the message buffer controlling part(8); a message pointer FIFO means(10); a free list FIFO means(11) storing the start address of the reassembly buffer(7); and a register(12) transmitting the information about the operation mode, the operation state and the interrupt of each control block.

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